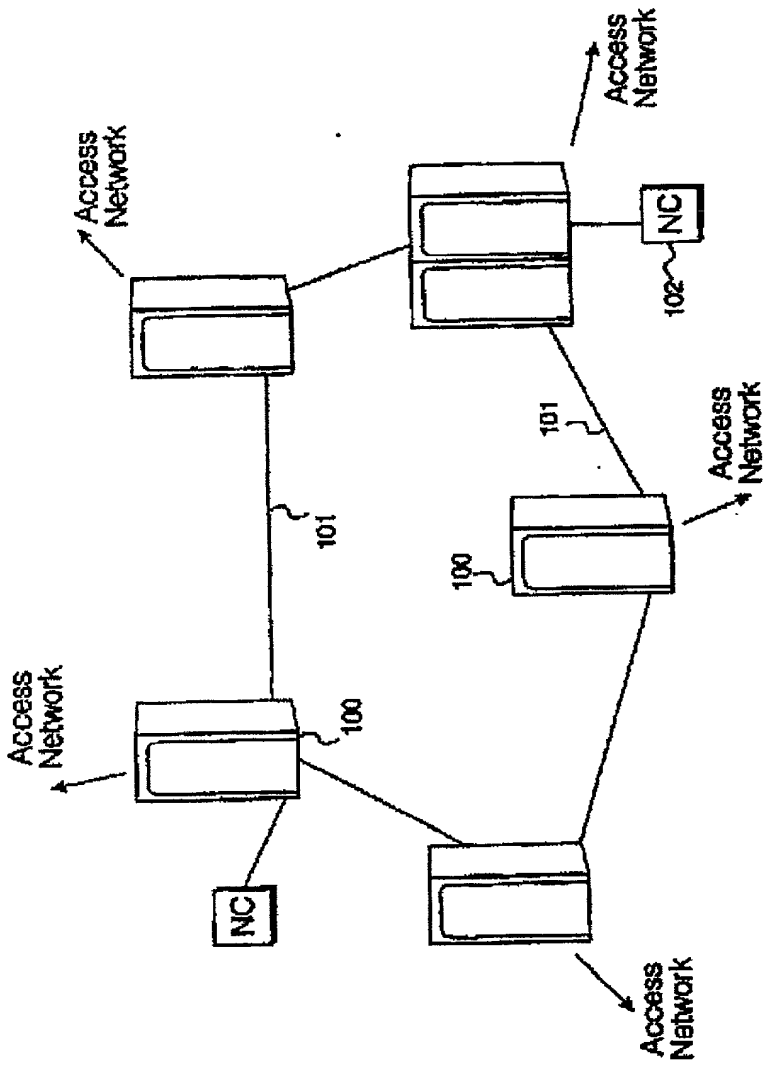
Access Network
Access Network
Access Network
Access Network
Access Network
NC
NC
100
100
101
101
102

Fig. 1

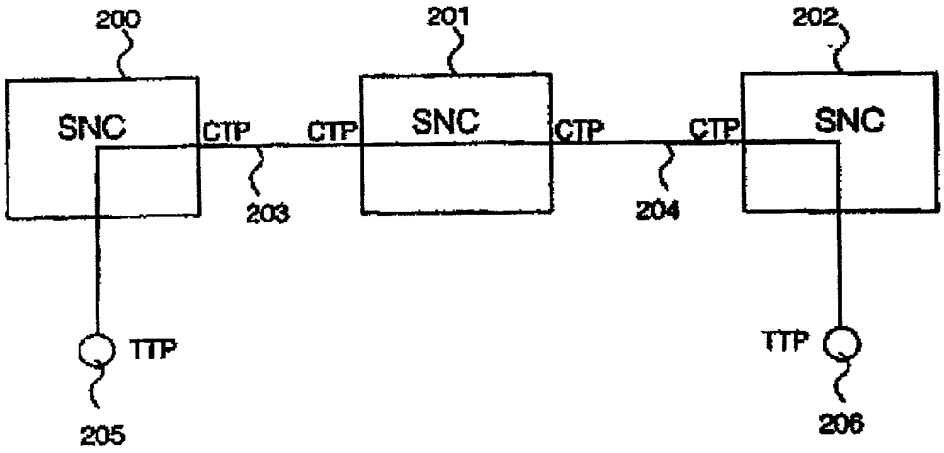

Fig. 2

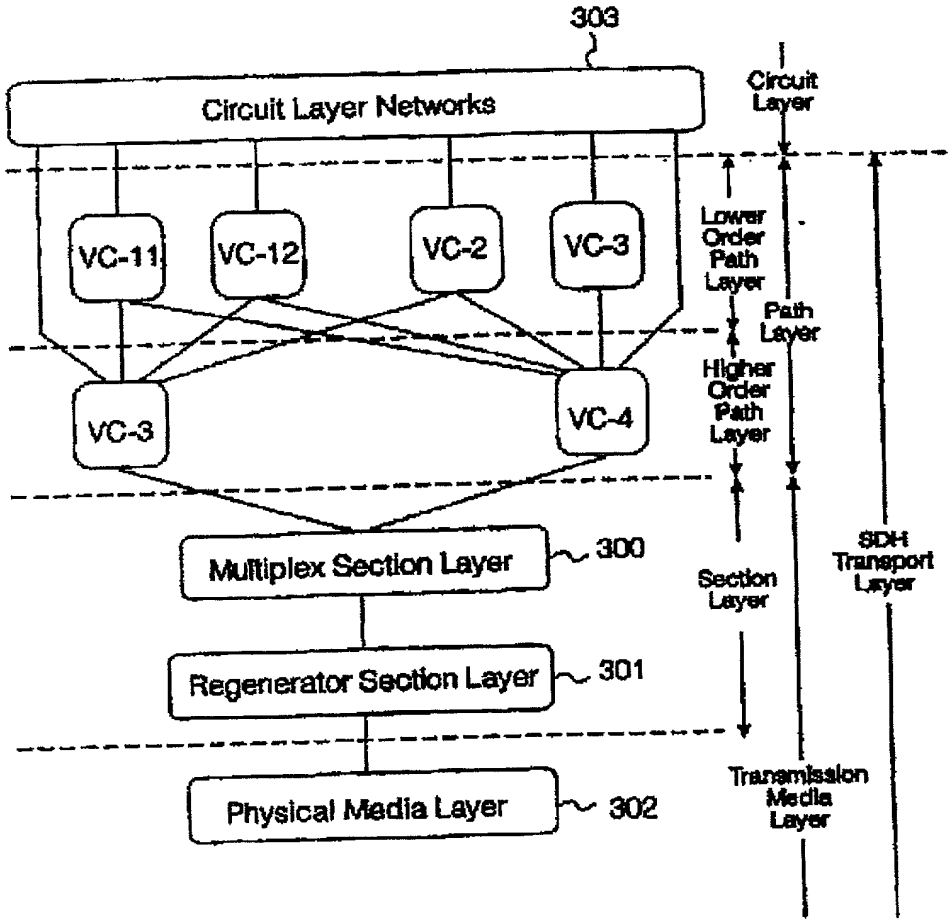
303
Circuit Layer Networks
Circuit Layer
VC-11
VC-12
VC-2
VC-3
Lower Order Path Layer
Path Layer
VC-3
VC-4
Higher Order Path Layer
Multiplex Section Layer
300
SDH Transport Layer
Section Layer
Regenerator Section Layer
301
Physical Media Layer
302
Transmission Media Layer

Fig. 3

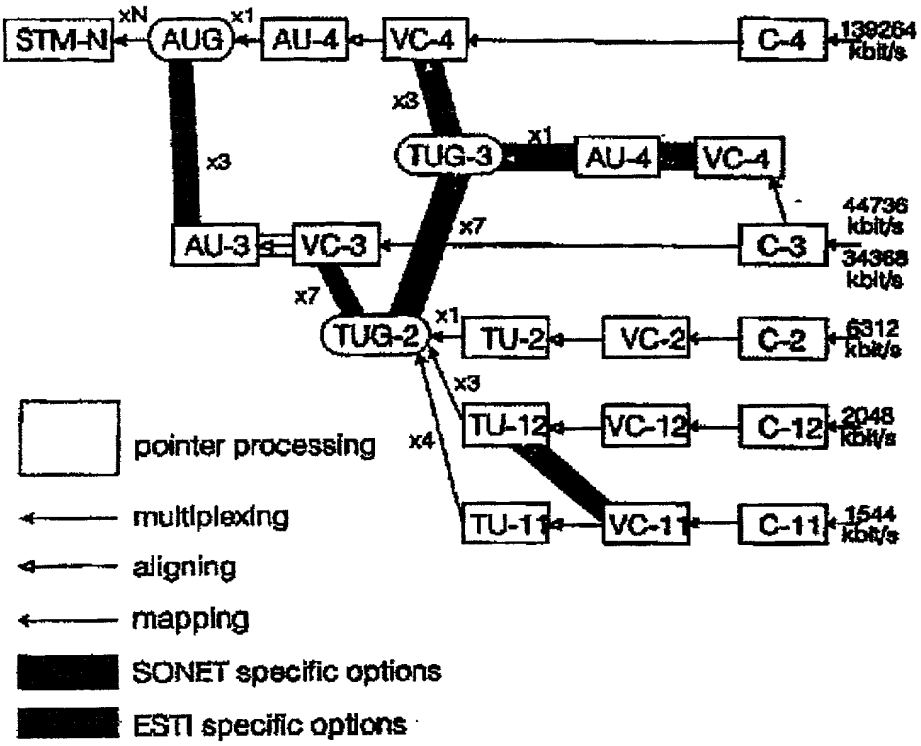
STM-N
xN
AUG
x1
AU-4
VC-4
C-4
139264 kbit/s
x3
TUG-3
x1
AU-4
VC-4
x3
AU-3
VC-3
x7
C-3
44736 kbit/s
34368 kbit/s
x7
TUG-2
x1
TU-2
VC-2
C-2
6312 kbit/s
x3
TU-12
VC-12
C-12
2048 kbit/s
x4
TU-11
VC-11
C-11
1544 kbit/s
pointer processing
multiplexing
aligning
mapping
SONET specific options
ESTI specific options

Fig 4

2003-10-16 15:50:04

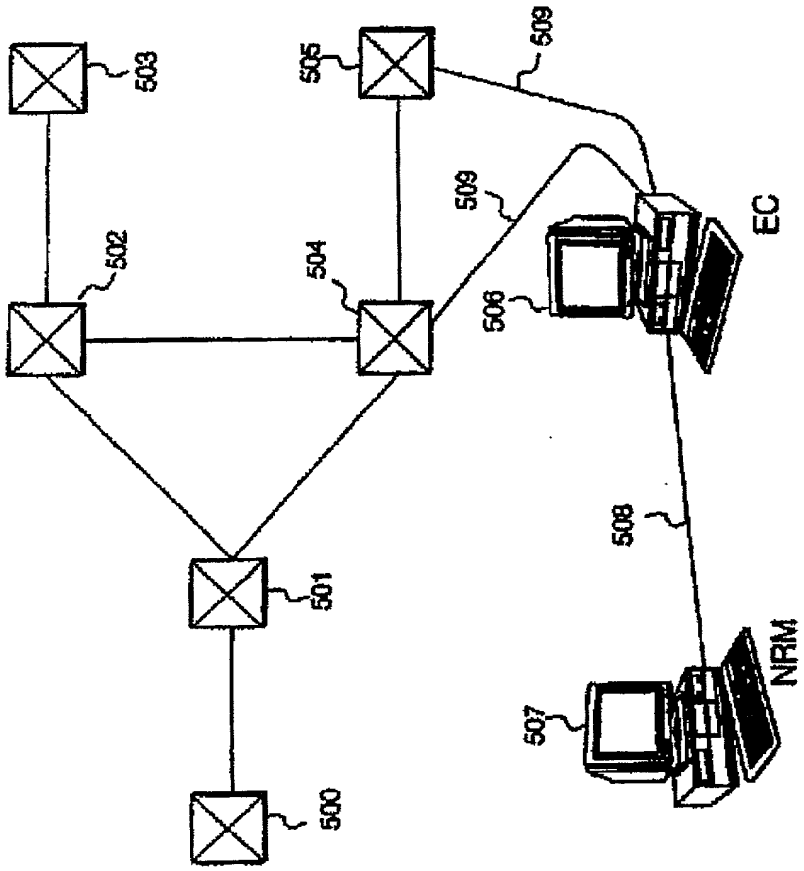

Fig. 5

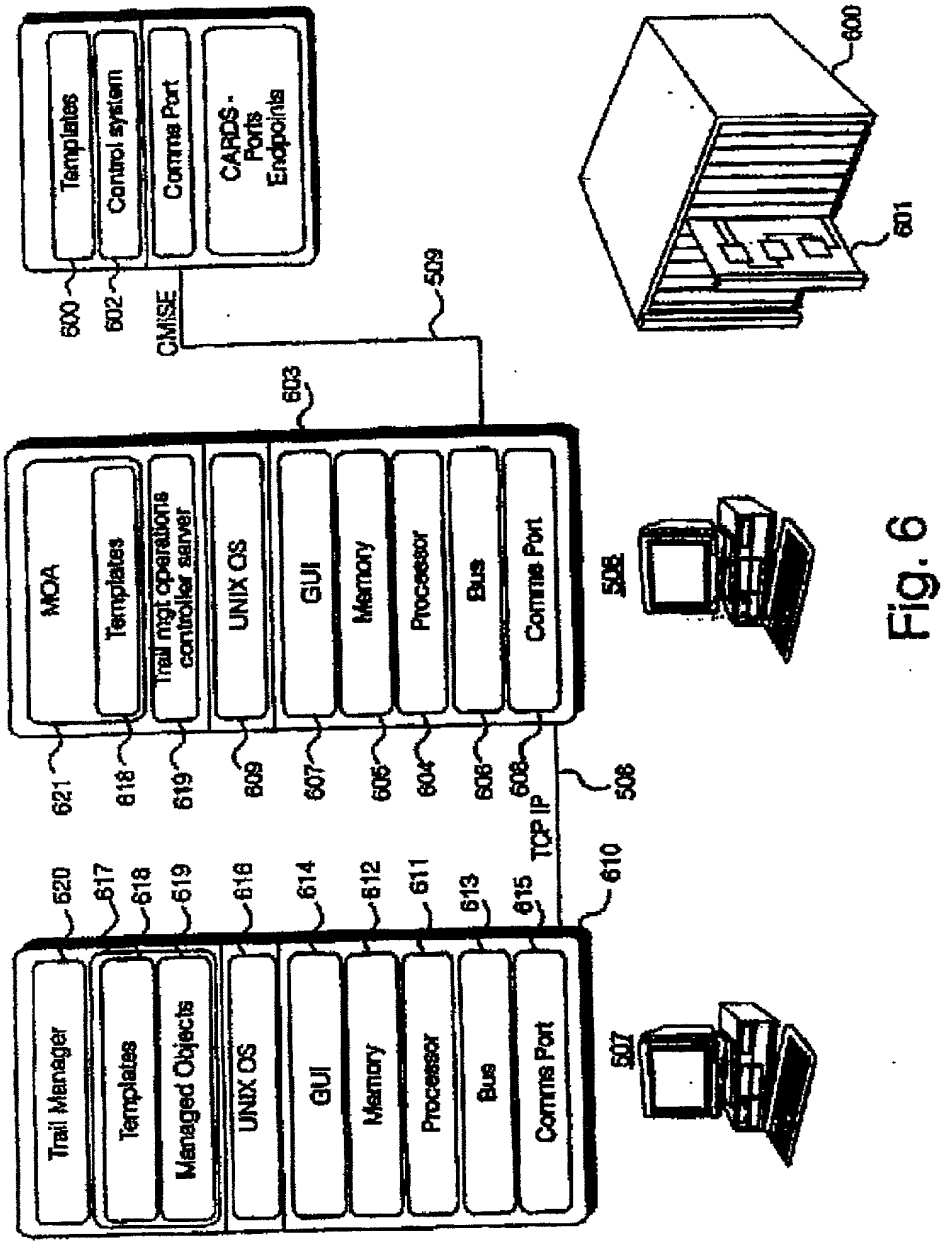
Templates
Control system
Comms Port
CARDS - Ports Endpoints
600
602
CMISE
509
603
MOA
Templates
Trail mgt operations controller server
UNIX OS
GUI
Memory
Processor
Bus
Comms Port
621
618
619
609
607
605
604
606
608
TCP IP
506
610
615
613
611
612
614
616
619
618
617
620
Trail Manager
Templates
Managed Objects
UNIX OS
GUI
Memory
Processor
Bus
Comms Port
507
505
600
601

Fig. 6

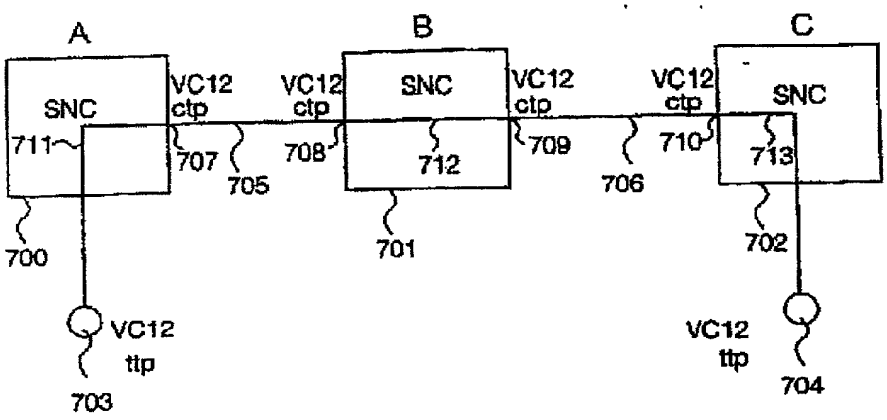
A
B
C
SNC
SNC
SNC
VC12
ctp
VC12
ctp
VC12
ctp
VC12
ctp
711
707
705
708
712
709
706
710
713
700
701
702
VC12
ttp
VC12
ttp
703
704

Fig. 7

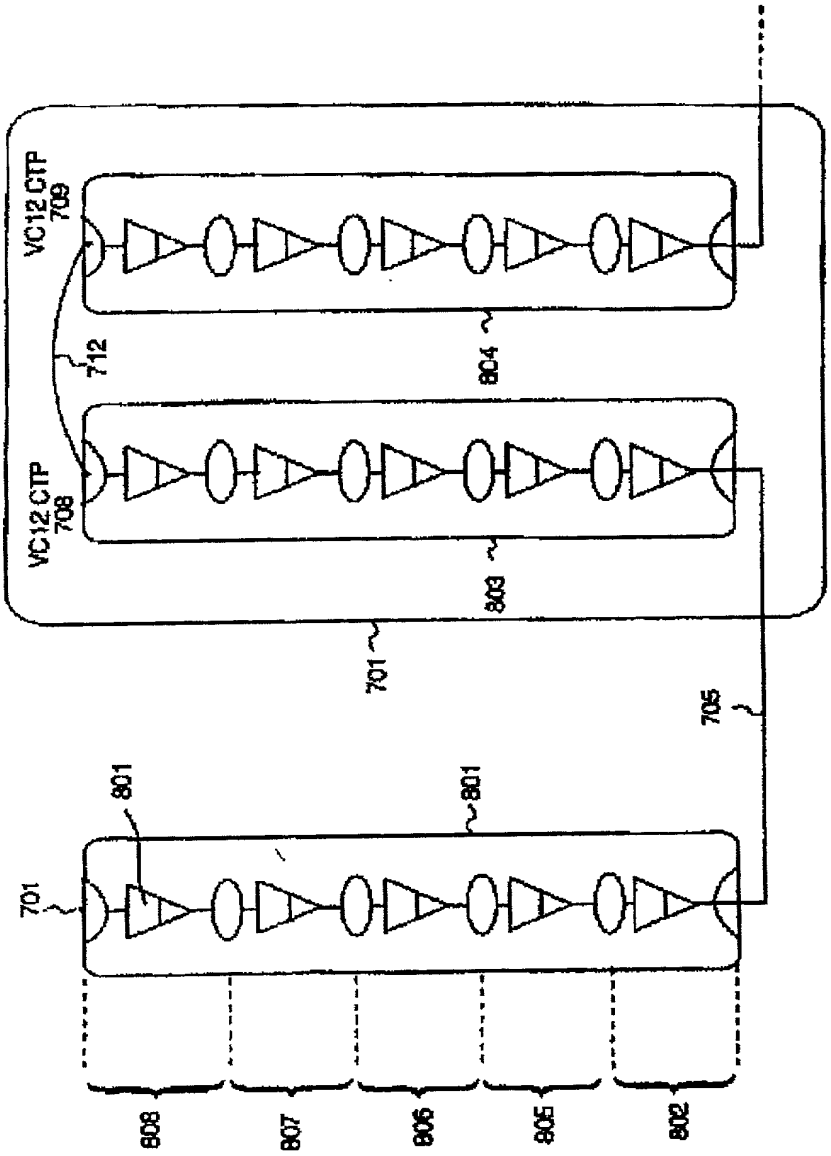
VC12 CTP
709
VC12 CTP
708
712
804
803
701
705
801
801
701
808
807
806
805
802

Fig. 8

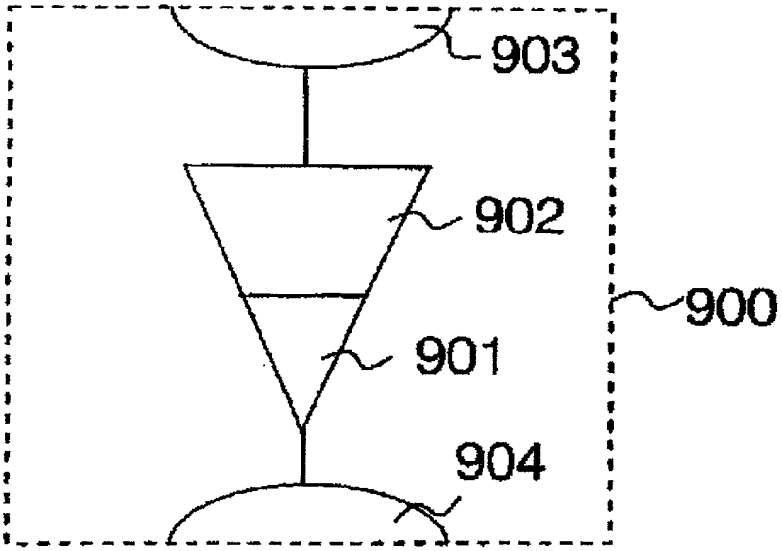

Fig. 9

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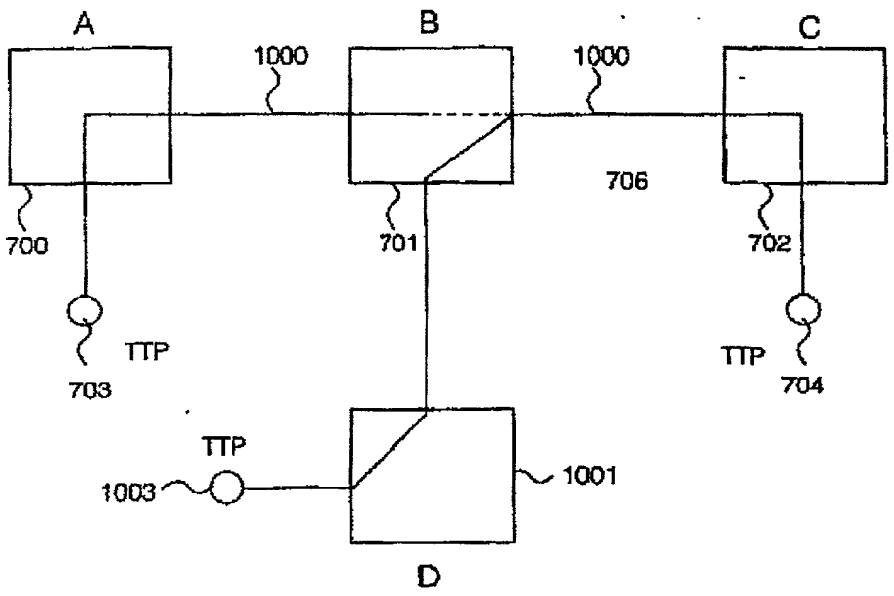

Fig. 10

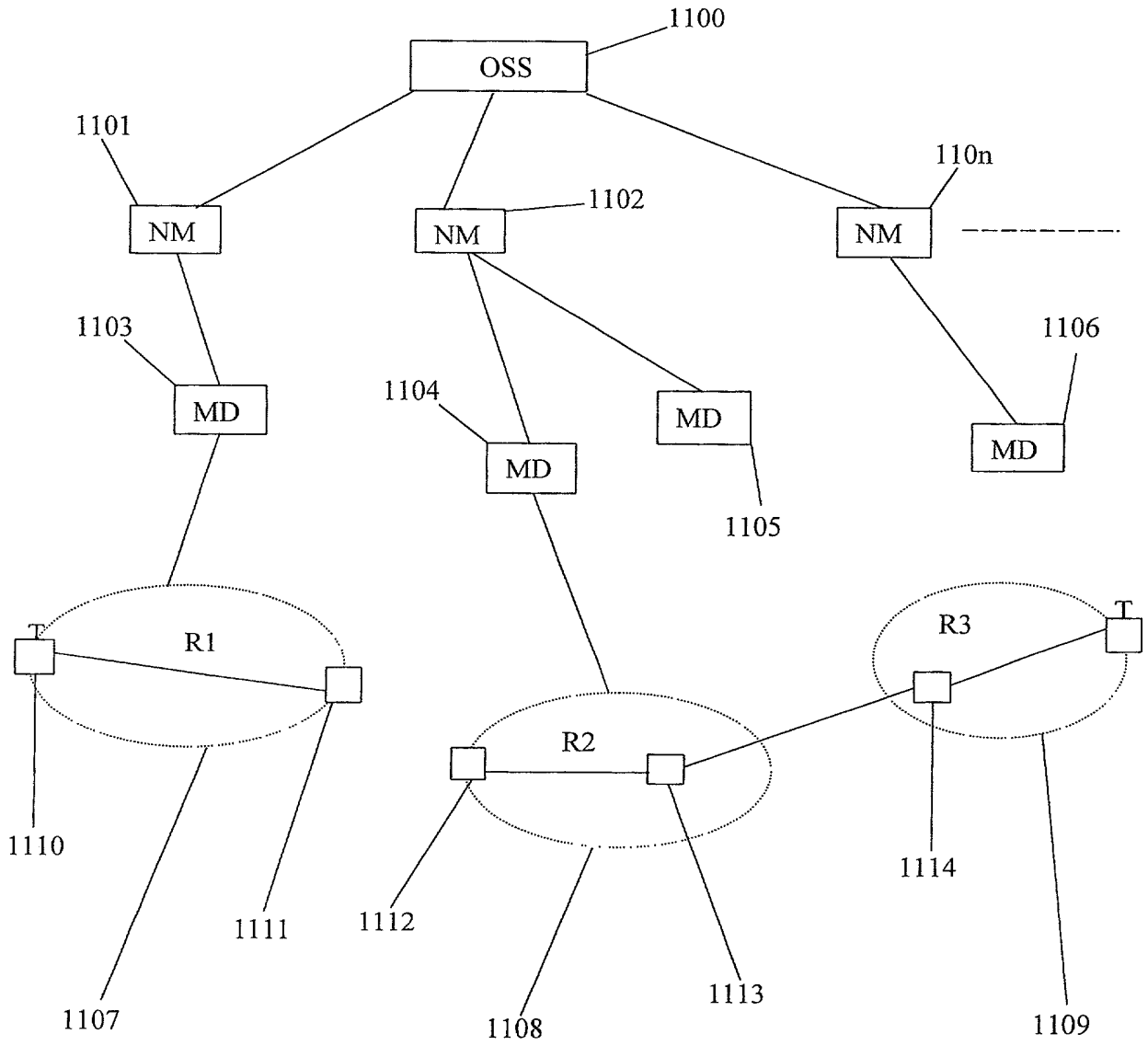
1100
OSS
1101
110n
NM
1102
NM
NM
1103
1106
MD
1104
MD
MD
MD
1105
T
R1
R3
T
R2
1110
1114
1111
1112
1113
1107
1108
1109

Fig 11

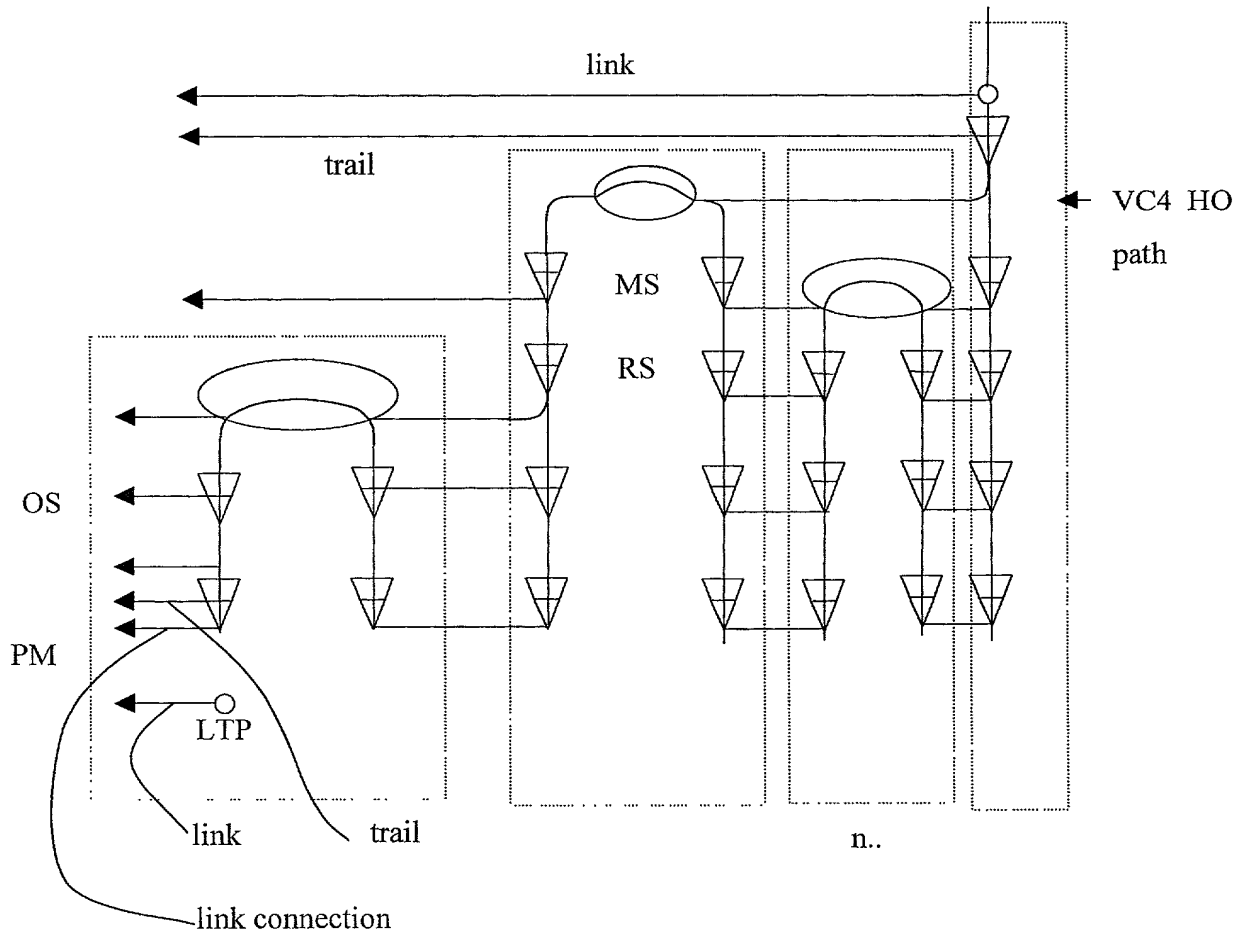
link
trail
VC4 HO
path
MS
RS
OS
PM
LTP
link
trail
n..
link connection

Fig 12

Off network
X
Y

Fig 13

annotate
link
LTP
VC 4
trail
TTP
MS
RS
OS
Off network
PMS
Edge node "X"

Fig 14

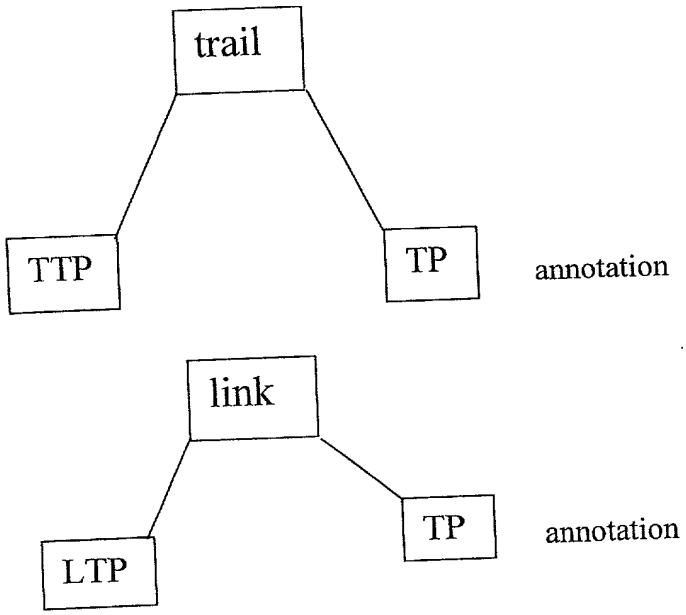

Fig 15

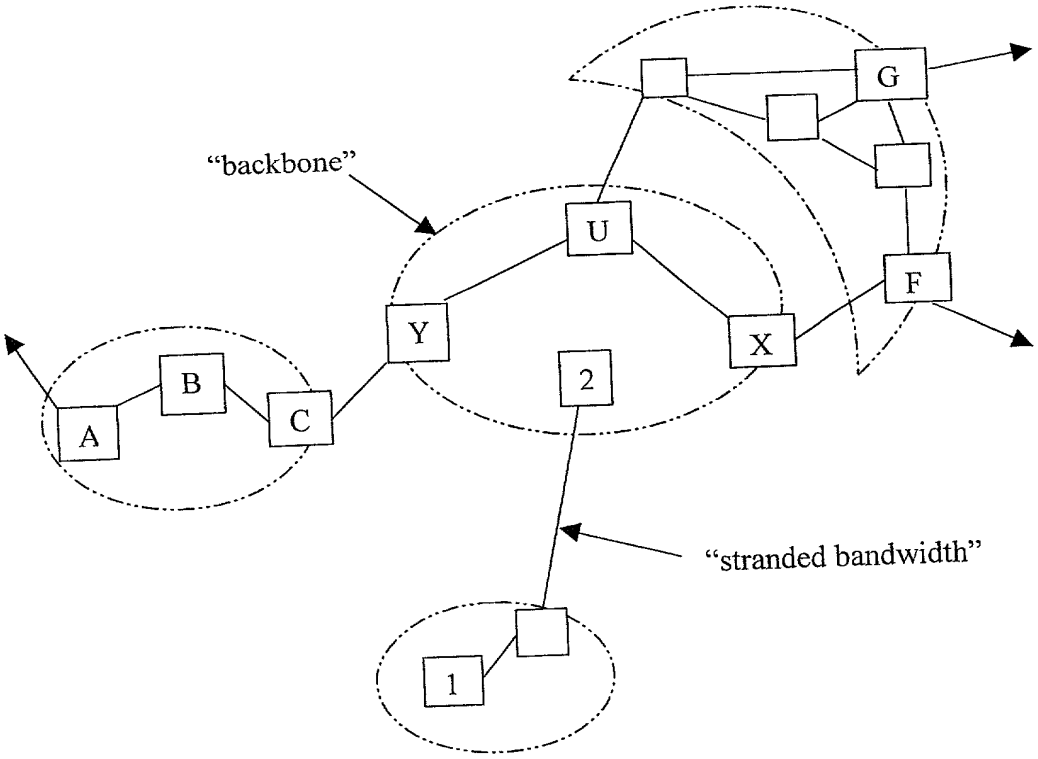

Fig 16

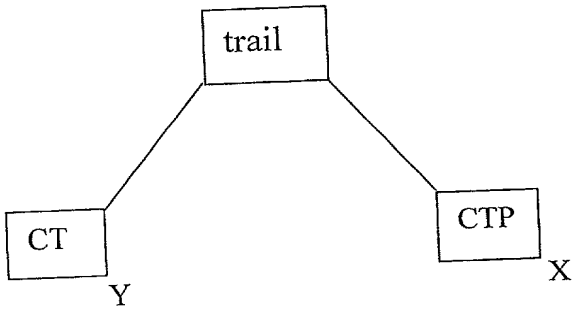

Fig 17